

IN THE SPECIFICATION

Please amend the first full paragraph on page 3, beginning at line 5 as follows:

--According to another aspect of the invention, a system for enabling recovery of lost payload blocks in a packet switch network includes: a demodulator configured to receive and demodulate a modulated signal to generate a sequence of demodulated packets, each packet in the sequence having a predetermined number of payload blocks; a decoder operatively coupled to the demodulator for decoding the demodulated packets into a plurality of decoded frames; a processor coupled to the decoder and configured to receive and check the plurality of decoded frames to identify a number of erroneously received payload blocks within a particular decoded frame; a storage means for storing other payload blocks that are successfully received within a particular frame in a storage medium for subsequent retrieval; a transmitter for subsequently transmitting a request for the retransmission of the particular frame [[with]] which in the previous transmission contained the erroneously received blocks; and, a combiner for combining in sequential order the stored payload blocks with ~~the erroneously received payload block~~ correctly received payload blocks of the retransmitted frame which were previously erroneously received ~~retrieved from the subsequent transmission in sequential order.~~ --

Please amend the last full paragraph on page 5, beginning at line 16 as follows:

-- In addition to the FEC and modulation schemes, digital communication systems according to the present invention employ automatic retransmission request (ARQ) techniques, which enable ~~the erroneously received information,~~ which was previously erroneously received, to be subsequently retransmitted to the receiver. ARQ techniques involve analyzing received blocks of data for error and requesting retransmission of blocks, which contain any error. The FEC techniques include, for example, convolutional or block coding of the data prior to modulation. It is common to refer to convolutional codes by their code rates, i.e., 1/2 and 1/3, wherein the lower code rates provide greater error protection but lower user bit rates for a given channel bit rate. Hence, the FEC coding involves representing a certain number of data bits using a certain number of code bits. It should be noted that the FEC techniques are well known to those skilled in this art.--